

10/510388

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DT04 Rec'd PCT/PTO 07 OCT 2004

PATENT

Docket No.: 60188-970

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Application of	:	Customer Number: 20277
Masayuki OZASA, et al.	:	Confirmation Number: Not yet assigned
Application No.: Not yet assigned	:	Group Art Unit: Not yet assigned
Filed: October 07, 2004	:	Examiner: Not yet assigned
For: DIFFERENTIAL AMPLIFIER AND OPERATIONAL AMPLIFIER	:	

**INFORMATION DISCLOSURE STATEMENT**

Honorable Commissioner for Patents and Trademarks  
Washington, D. C. 20231

Sir:

In accordance with the provisions of 37 C.F.R. 1.56, 1.97 and 1.98, the attention of the Patent and Trademark Office is hereby directed to the references listed on the attached form PTO-1449. It is respectfully requested that the references be expressly considered during the prosecution of this application, and that the references be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

This Information Disclosure Statement is being filed with the application and no certification or fee is required.

A copy of the foreign search report is attached for the Examiner's information. Please note this is a PCT application in the entry of the National Phase in the U.S. and copies of the references cited were transmitted by WIPO and are believed to be in the file of the above identified application and readily available to the Examiner. Therefore it is believed that Applicants have met all requirements regarding duty of disclosure under 37

Not yet assigned

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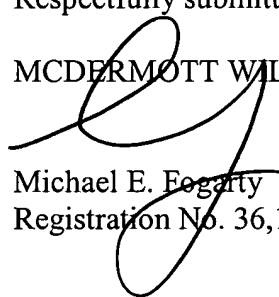
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CFR 1.56. Acknowledgement and consideration of these documents are respectfully requested.

A copy of the foreign search report is attached for the Examiner's information. Please note this is a PCT application in the entry of the National Phase in the U.S. Since the Search Report was from the U.S. JPO or EPO search authorities, copies of these references should have been supplied to the USPTO under the trilateral agreement and are believed to be in the file of the above identified application and readily available to the Examiner.

Respectfully submitted,

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<b>INFORMATION DISCLOSURE CITATION IN AN APPLICATION</b>  <b>(PTO-1449)</b>				ATTY. DOCKET NO. <b>60188-970</b>		SERIAL NO. Not yet assigned <div style="font-size: 1.5em; font-weight: bold;">10/510388</div>	
				APPLICANT <b>Masayuki OZASA, et al.</b>			
				FILING DATE <b>October 07, 2004</b>		GROUP Not yet assigned	
U.S. PATENT DOCUMENTS							
EXAMINER'S INITIALS	CITE NO.	Document Number Number-Kind Code <sup>2</sup> (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear		
		US					
		US					
		US					
		US					
		US					
		US					
FOREIGN PATENT DOCUMENTS							
EXAMINER'S INITIALS	CITE NO.	Foreign Patent Document Country Codes - Number & -Kind Codes (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines Where Relevant Figures Appear	Translation	
						Yes	No
		JP 11-500883 A	01/19/1999	-	Corresponds with PCT International Publication No. WO 97/06595	JAPAN (w/English Translation of Claims)	
		WO 97/06595	02/20/1997	PHILIPS ELECTRONICS N.V.	Corresponds with JP 11-500883	X	
		JP 8-18355 A	01/19/1996	ASAHI KASEI MICRO SYST KK		JAPAN (w/English Abstract)	
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		JP 6-90125 A	03/29/2004	FUJITSU TEN LTD.			
		JP 9-74322 A	03/18/1997	FUJI PHOTO FILM CO., LTD.			
		JP 11-355066 A	12/24/1999	SONY CORP.			
		JP 6-169227 A	06/14/1994	MITSUBISHI ELECTRIC CORP.			
OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)							
EXAMINER'S INITIALS	CITE NO.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.					
		TAMAI, Tokumichi., "Semiconductor Circuit Design Technology." Nikkei BP, Inc., First Edition, November 16, 1990, pp.308-311 (Including Partial English Translation)					
		GRAY, Paul, R., et al. "Analysis and Design of Analog Integrated Circuits." John Wiley & Sons, Inc. Third Edition. Chapter 3, Single Transistor and Two-Transistor Amplifiers, 1993, pp.244-249					
EXAMINER				DATE CONSIDERED			

\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

1 Applicant's unique citation designation number (optional). 2 Applicant is to place a check mark here if English language Translation is attached.